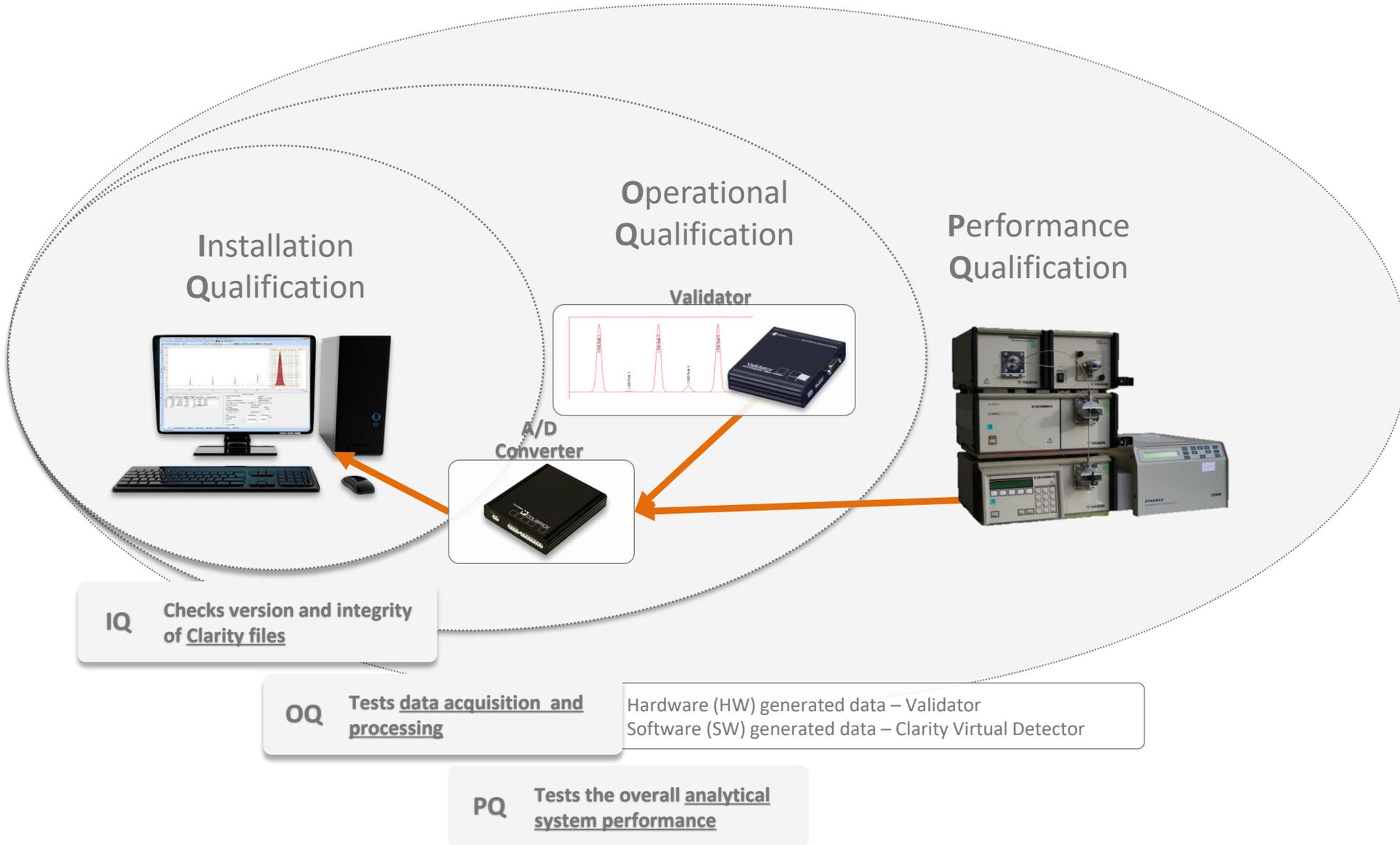




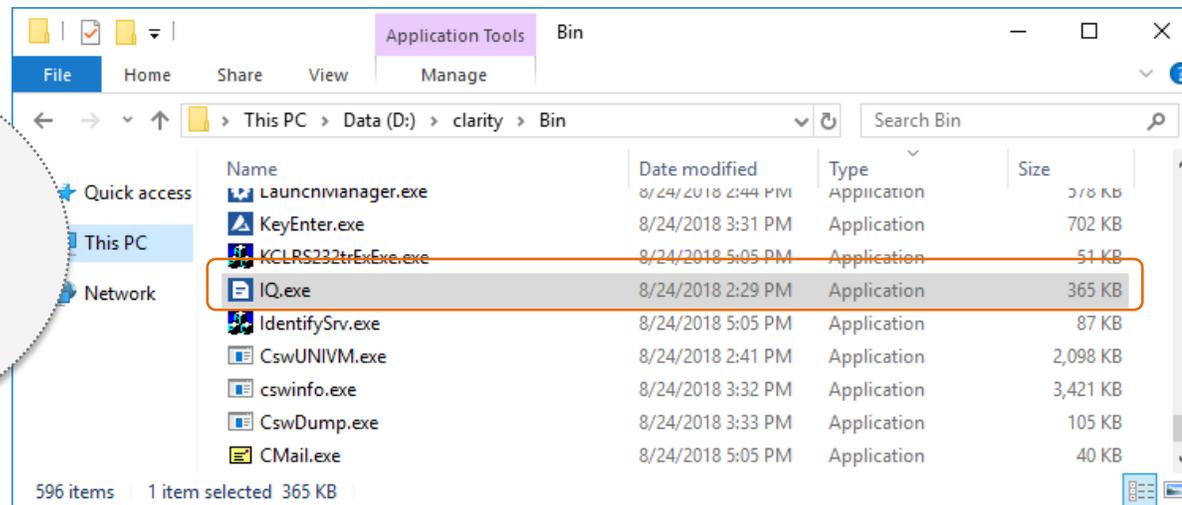
# VALIDATION

P019/80F 01/2023



# Installation Qualification (IQ)

- ➔ The IQ checks the **version** and **integrity** of Clarity files for correct functionality
- ➔ Run the **IQ utility** from the Clarity folder - an **IQ Report will be produced**





IQ

File Help

## Installation Qualification Report

Date	20.04.2020, 09:58
Serial number of application	088-000888+38911/00
User Code	CW9PJKEJEFBKPH7R
Version of application	Clarity version 8.3.0.127
Build date of application	10.04.2020, 01:51
Instruments	All
Extensions	SST; GPC; PDA; EA; CE; MS; NGA; DHA; GCxGC; MS-TOF
Controls	GC; LC; AS
Certification file	C:\Clarity83\bin\liq.chk
Checksum of cert. file	C7E8303812AD86C2
Date of cert. file	10.04.2020, 04:08
User	marekp
System	Microsoft Windows 10 Professional version 10.0 (Build 18362)
Acquisition and hardware devices	Key Rockey 4ND 088-00888+1073780735/000 id:2FE9D8DA DEMO Virtual detector <Unknown> Collbrick 1284 Virtual Digital Input Output Loop

**Core Files, Embedded Components: Failed**

Files

[Show files list >](#)

**3<sup>rd</sup> Party Packages**

No external packages installed.

Checked by: ..... Signature: .....

IQ

File Help

## Installation Qualification Report

Date	20.04.2020, 09:58
Serial number of application	088-000888+38911/00
User Code	CW9PJKEJEFBKPH7R
Version of application	Clarity version 8.3.0.127
Build date of application	10.04.2020, 01:51
Instruments	All
Extensions	SST; GPC; PDA; EA; CE; MS; NGA; DHA; GCxGC; MS-TOF
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Date of cert. file	10.04.2020, 04:08
User	marekp
System	Microsoft Windows 10 Professional version 10.0 (Build 18362)
Acquisition and hardware devices	Key Rockey 4ND 088-00888+1073780735/000 id:2FE9D8DA DEMO Virtual detector <Unknown> Collbrick 1284 Virtual Digital Input Output Loop

**Core Files, Embedded Components: Failed**

Files

[Hide files list <](#)

File	Path	Version	Size	File date	Status
advantecchf122scfc.rb	c:\clarity83\bin\utils\uni_drivers\advantec	-	36355	25.09.2015, 21:26	Failed: bad checksum
AdvionCMS.dll	C:\Clarity83\bin	6.4.14.1	487936	10.04.2020, 03:51	Passed
advioncommon.dll	c:\clarity83\bin	6.4.14.1	30208	10.04.2020, 03:51	Passed
adviondata.dll	c:\clarity83\bin	6.4.14.1	315392	10.04.2020, 03:51	Passed
advionobjects.dll	c:\clarity83\bin	6.4.14.1	935424	10.04.2020, 03:51	Passed
as500pump.rb	c:\clarity83\bin\utils\uni_drivers\asi	-	46586	10.04.2020, 01:44	Passed
aspen.dll	c:\clarity83\bin	1.2.4.0	185344	10.04.2020, 03:51	Passed
beep.exe	c:\clarity83\bin\utils	-	34816	10.04.2020, 02:37	Passed
cfgcnt.dll	c:\clarity83\bin	8.1.0.6036	38400	10.04.2020, 03:51	Passed
cfgcnt.dll	c:\clarity84\bin	8.1.0.6036	38400	08.04.2020, 03:01	Passed
CfgCntProxy.dll	C:\Clarity83\bin	8.1.0.6036	45568	10.04.2020, 03:51	Passed
cfgcntproxy.dll	c:\clarity84\bin	8.1.0.6036	45568	08.04.2020, 03:01	Passed
cfgcntsrv.exe	c:\clarity83\bin	8.1.0.6036	56832	10.04.2020, 03:51	Passed
chromophor2010pump.rb	c:\clarity83\bin\utils\uni_drivers\chromophor	-	42263	10.04.2020, 01:44	Passed
clcrocclooven.rb	c:\clarity83\bin\utils\uni_drivers\cluzeau	-	36211	10.04.2020, 01:44	Passed
cligcko2000oven.rb	c:\clarity83\bin\utils\uni_drivers\cluzeau	-	36457	10.04.2020, 01:44	Passed
cliglocclooven.rb	c:\clarity83\bin\utils\uni_drivers\cluzeau	-	36195	10.04.2020, 01:44	Passed
clarity.exe	c:\clarity83\bin	8.3.0.127	32028160	10.04.2020, 01:51	Passed
cmail.exe	c:\clarity83\bin	1.0.0.1	40960	10.04.2020, 03:51	Passed

- General information about your Clarity installation
- Indicates if the installation has **Passed**, **Passed with warnings** or **Failed** the test
- Indicates which files have passed or failed

# Operational Qualification (OQ)

- Part of validation is the OQ which **tests data acquisition and processing**
- OQ is part of the tools supporting **21 CFR Part 11 Regulations**
- **System Suitability Test (SST)** extension is required
- **Validation Kit** is required only for station with A/D converters



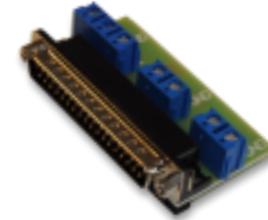
Validation Kit manual



Validator Certificate



Terminal Board



Cable for the A/D Converter

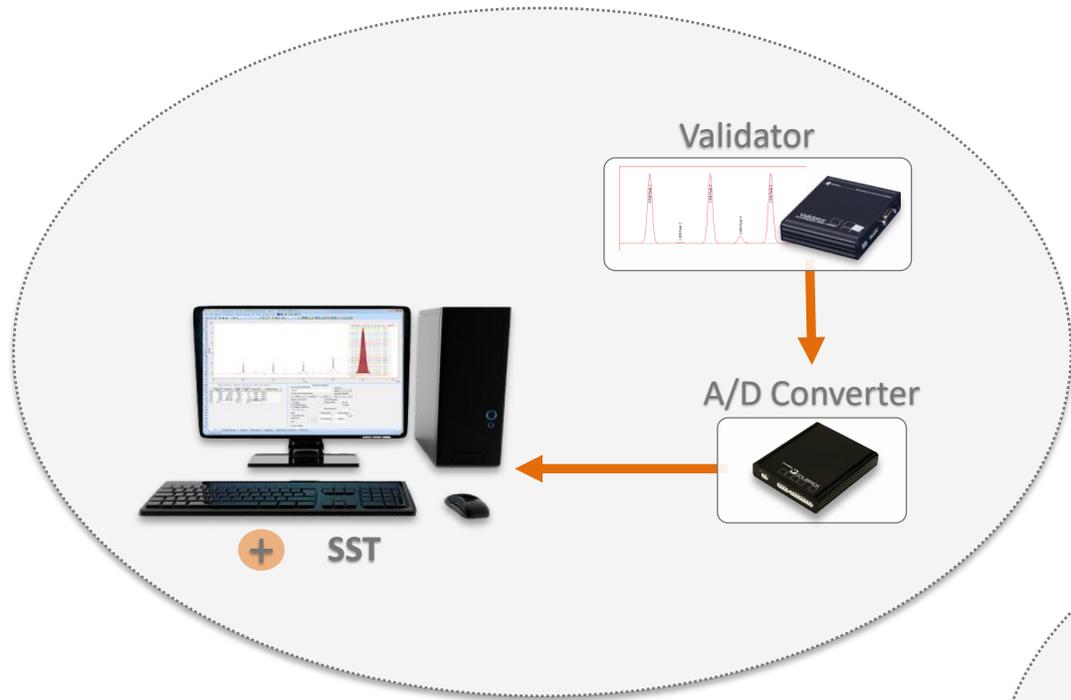


AC/DC Power Adapter



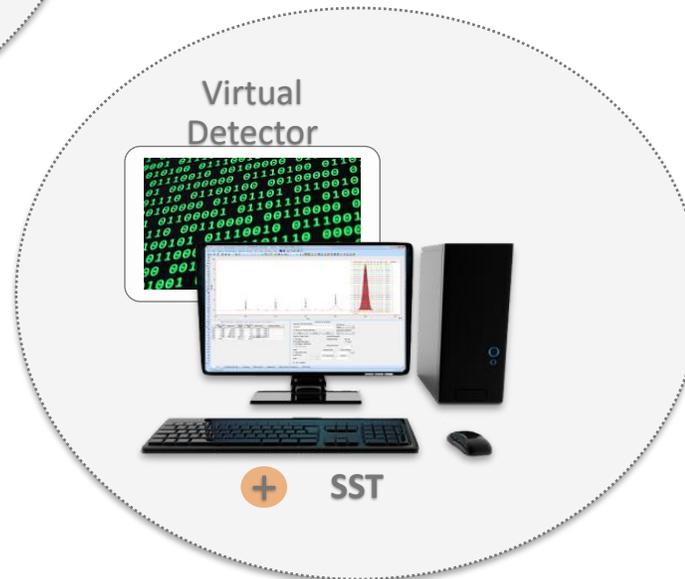
Validator





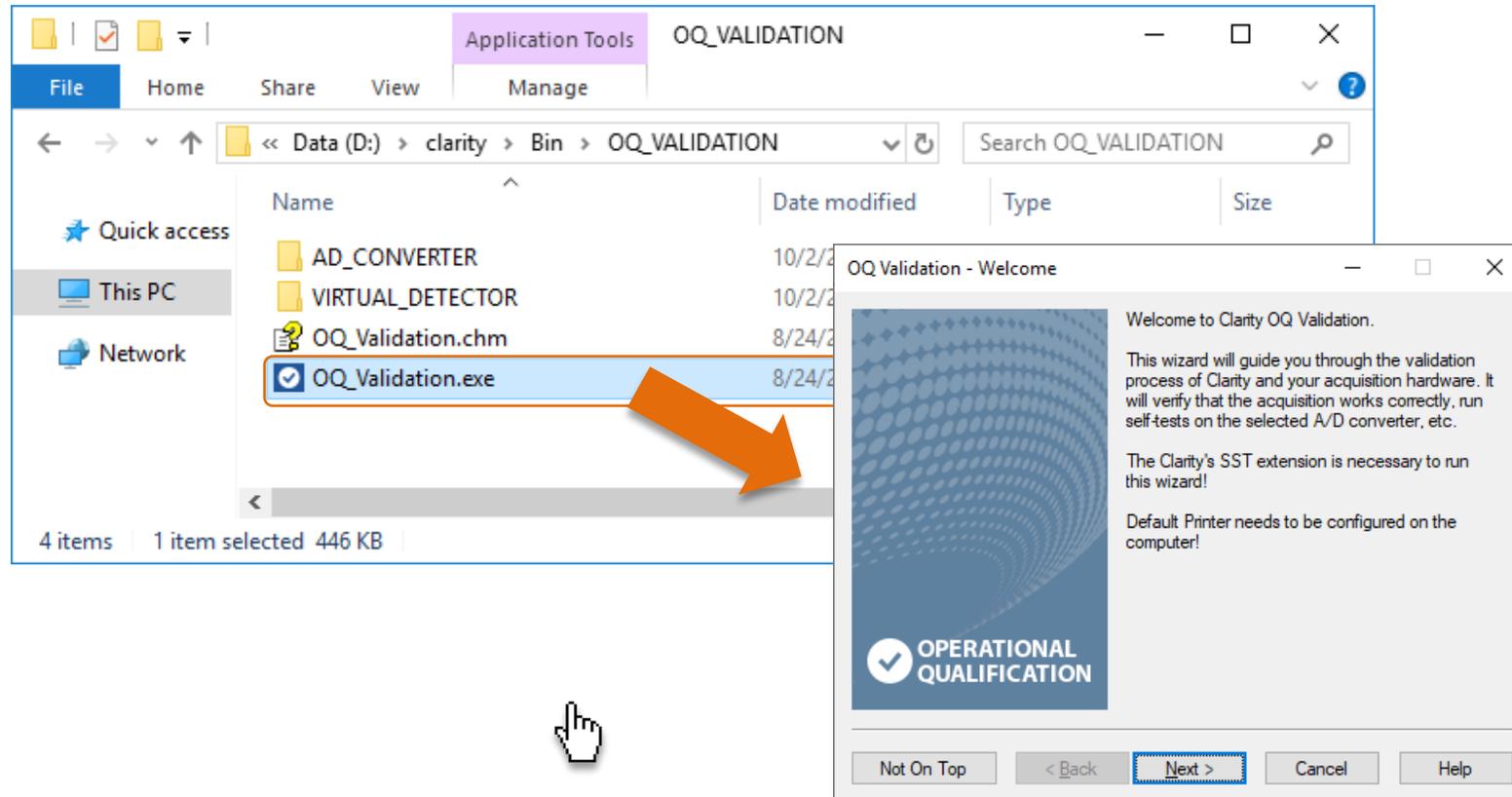
→ Chromatograms are either generated by our **Validator (HW)**

→ ... or via **Virtual Detector (SW)** from Clarity





→ To start the OQ test, run the OQ\_Validation wizard in the Clarity – OQ\_Validation folder





OQ Validation - Validation Type

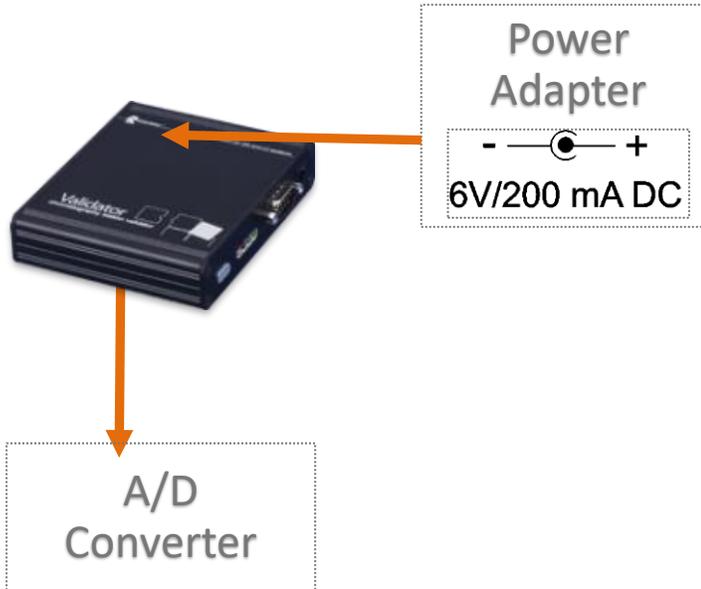
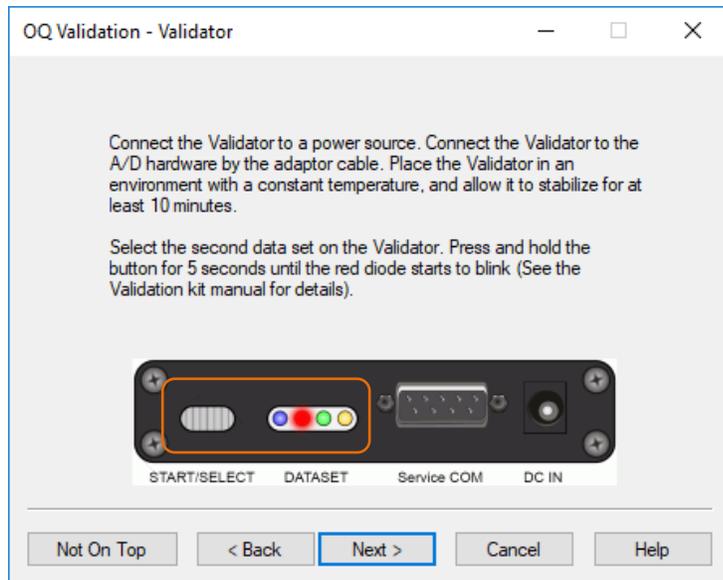
Select validation type

Validation with an A/D converter (HW & SW Validation)

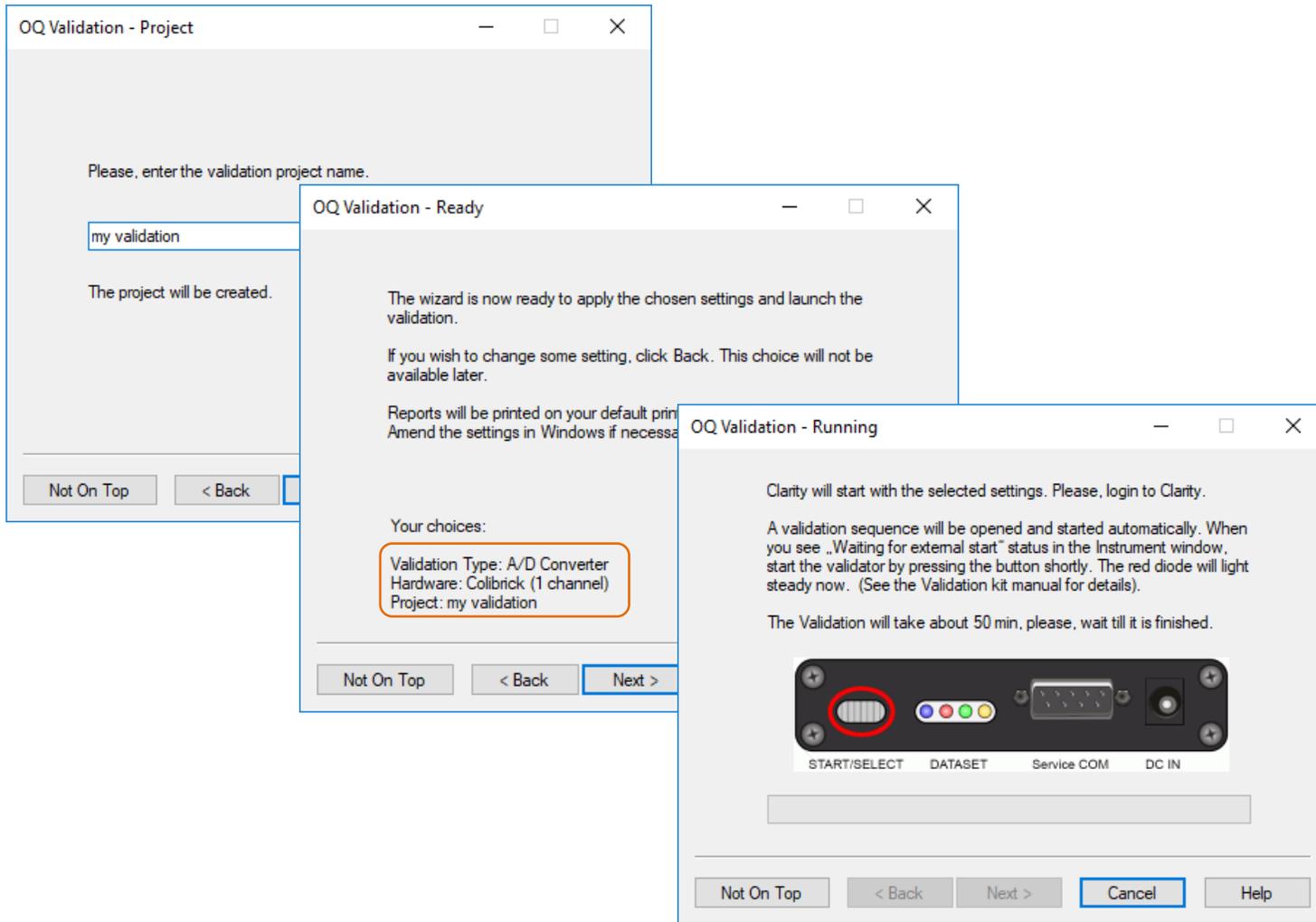
Validation with a Virtual detector (SW Validation only)

Not On Top   < Back   Next >   Cancel   Help

- Select if you want to validate:
- With a **Validator** (validation of SW & A/D converter)
- or with Clarity **Virtual Detector** (validation of SW only)
- the only feasible way for instruments with **digital acquisition** (see Validation Kit manual)



- If you selected the **Validator**
- **Power** it and connect it to the **A/D converter**
- **Press & hold** the button for 5 sec to select the **2nd dataset (red)**



- Enter the validation project name
- Confirm your settings or click back if you need to change them
- Then login to Clarity and press the **Start button on the Validator** to start the validation

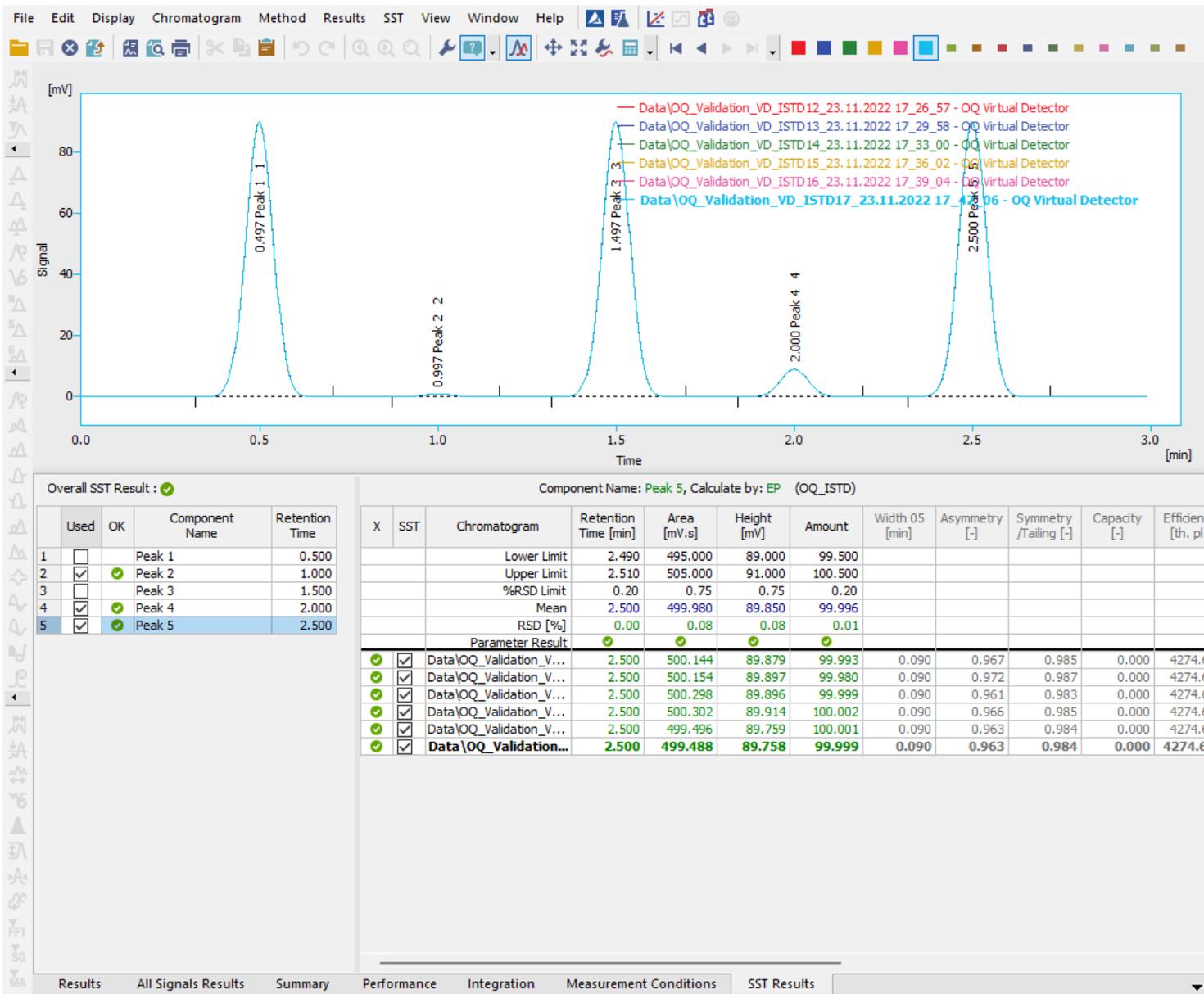


- The OQ will run a **sequence of analyses**
- Each set of analyses, **tests** a different set of **parameters**
- Each **set** is associated with a **method** and a **report style**

	Status	Run	SV	EV	I/V	Sample ID	Sample	Sample Amount	ISTD1 Amount	Sample Dilut.	Inj. Vol. [µL]	File Name	Sample Type	Lvl	Method Name	Report Style	Open	Open Calib.	Print	Print to PDF	Run Program	Program To Run	Parameters	
1	✓	✓	1	1	1	STD1		0.000	0.000	1.000	0.000	%e_%q_%2v_%d_%3n	Stan	1	OQ_CAL		<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>			
2	✓	✓	2	2	1	STD2		0.000	0.000	1.000	0.000	%e_%q_%2v_%d_%3n	Stan	2	OQ_CAL		<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>			
3	✓	✓	3	3	1	STD3		0.000	0.000	1.000	0.000	%e_%q_%2v_%d_%3n	Stan	3	OQ_CAL		<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>			
4	✓	✓	4	4	1	STD4		0.000	0.000	1.000	0.000	%e_%q_%2v_%d_%3n	Stan	4	OQ_CAL		<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>			
5	✓	✓	5	5	1	STD5		0.000	0.000	1.000	0.000	%e_%q_%2v_%d_%3n	Stan	5	OQ_CAL	OQ_Lin...	<input type="checkbox"/>	<input type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>		
6	✓	✓	6	6	1	ESTD		0.000	0.000	1.000	0.000	%e_%q_%2v_%d_%3n	Unkn		OQ_ESTD		<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input checked="" type="checkbox"/>	%Self%	"%ProjectDir%\OQ_e	
7	✓	✓	7	7	1	ESTD		0.000	0.000	1.000	0.000	%e_%q_%2v_%d_%3n	Unkn		OQ_ESTD		<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>			
8	✓	✓	8	8	1	ESTD		0.000	0.000	1.000	0.000	%e_%q_%2v_%d_%3n	Unkn		OQ_ESTD		<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>			
9	✓	✓	9	9	1	ESTD		0.000	0.000	1.000	0.000	%e_%q_%2v_%d_%3n	Unkn		OQ_ESTD		<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>			
10	✓	✓	10	10	1	ESTD		0.000	0.000	1.000	0.000	%e_%q_%2v_%d_%3n	Unkn		OQ_ESTD		<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>			
11	✓	✓	11	11	1	ESTD		0.000	0.000	1.000	0.000	%e_%q_%2v_%d_%3n	Unkn		OQ_ESTD	OQ_EST	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>			
12	✓	✓	12	12	1	ISTD1		0.000	100.000	1.000	0.000	%e_%q_%2v_%d_%3n	Unkn		OQ_ISTD1		<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input checked="" type="checkbox"/>	%Self%	"%ProjectDir%\OQ_i	
13	✓	✓	13	13	1	ISTD1		0.000	100.000	1.000	0.000	%e_%q_%2v_%d_%3n	Unkn		OQ_ISTD1		<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>			
14	✓	✓	14	14	1	ISTD2		0.000	100.000	1.000	0.000	%e_%q_%2v_%d_%3n	Unkn		OQ_ISTD2		<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>			
15	✓	✓	15	15	1	ISTD2		0.000	100.000	1.000	0.000	%e_%q_%2v_%d_%3n	Unkn		OQ_ISTD2		<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>			
16	✓	✓	16	16	1	ISTD3		0.000	100.000	1.000	0.000	%e_%q_%2v_%d_%3n	Unkn		OQ_ISTD3		<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>			
17	✓	✓	17	17	1	ISTD3		0.000	100.000	1.000	0.000	%e_%q_%2v_%d_%3n	Unkn		OQ_ISTD3	OQ_ISTD	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	%Self%	delayed_exit=60	
18																	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>			



# VALIDATION → OPERATIONAL QUALIFICATION → WIZARD



- Once the data is **acquired** and **integrated**, the **SST** extension will check if the parameters fall within the set limits
- **Overall results** are clearly **indicated** in the **OK** column



# VALIDATION → OPERATIONAL QUALIFICATION → OQ REPORT

22.4.2016 12:17 Chromatogram C:\Clarity\Valdance\_2016\Data\IQ\_Validation\_upad2\_ESTD\_11\_22\_04\_2016\_047.PRM Page 1 of 4

**Clarity - Operational Qualification**  
**ESTD calculation test**  
 © DataApex 2009, www.dataapex.com

**TEST Result: Passed [ ] Signature:**

Chromatogram Info:  
 File Name : C:\Clarity\Valdance\_2016\Data\IQ\_Validation\_upad2\_ESTD\_11\_22\_04\_2016\_047.PRM  
 M  
 Origin : Acquired, Acquisition started 22.4.2016 12:14:17  
 Project : C:\Clarity\Projects\valdance\_2016.PRJ  
 Method : C:\Clarity\Valdance\_2016\IQ\_ESTD.MET  
 Computer : QCM8AD2  
 Clarity : Ver: 6.2.0.208, Demo: 0, SN: 00145316

Printed Version Info:  
 Printed Version : 22.4.2016 12:17:17 Recent (Linked Calibration)  
 Report Style : C:\Clarity\Common\IQ\_ESTD.rpt  
 Calibration File : C:\Clarity\Valdance\_2016\Calibr\IQ\_CAL.CAL  
 Computer : QCM8AD2  
 Clarity : Ver: 6.2.0.208, Demo: 0, SN: 00145316

22.4.2016 12:17 Chromatogram C:\Clarity\Valdance\_2016\Data\IQ\_Validation\_upad2\_ESTD\_11\_22\_04\_2016\_047.PRM Page 3 of 4

**Clarity - Operational Qualification**  
**ESTD calculation test**  
 © DataApex 2009, www.dataapex.com

**TEST Result: Passed [ ] Signature:**

SST Components Table

Used	OK	Component Name	Rate T/r
<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	Peak 1	1
<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	Peak 2	1
<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	Peak 3	1
<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	Peak 4	1
<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	Peak 5	1



Petržalkova 258V/13, 15860 Prague, The Czech Republic

Sample Info:  
 Sample ID : ESTD  
 Sample :  
 Inj. Volume [mL] : 0  
 Method : OQ\_ESTD  
 Description :  
 Created : 25.6.2004 14:53

Column :  
 Mobile Phase :  
 Flow Rate :  
 Note :  
 Autostop : 3.00 min  
 Detector 1 : U-PAD2 - 1  
 Detector 2 : U-PAD2 - 2  
 Subtraction Chromatogram : (None)

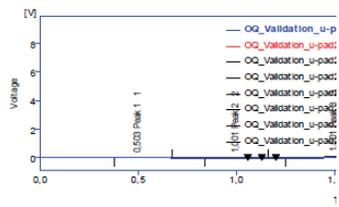
Base : Area Calibration File : OI  
 Scale Factor : Not Used Units After Scaling : %  
 Unretained Time : 0.00 min Column Length : 5C  
 Result Table Reports : All Identified Peaks Hide IDTD Peak : Er

SST Parameters Table  
 Calculated by Actual Values

X	SST	Chromatogram	Rate Time
		Lower Limit	1
		Upper Limit	1
		%RSD Limit	1
		Mean	1
		RSD (%)	1
		Parameter Result	1
<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	IQ_Validation_upad2_ESTD_11_22_04_2016_047 - U-PAD2 - 1	1
<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	IQ_Validation_upad2_ESTD_06_22_04_2016_042 - U-PAD2 - 1	1
<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	IQ_Validation_upad2_ESTD_07_22_04_2016_043 - U-PAD2 - 1	1
<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	IQ_Validation_upad2_ESTD_08_22_04_2016_044 - U-PAD2 - 1	1
<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	IQ_Validation_upad2_ESTD_09_22_04_2016_045 - U-PAD2 - 1	1
<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	IQ_Validation_upad2_ESTD_10_22_04_2016_046 - U-PAD2 - 1	1

Certificate for Clarity station Validation

Certificate No: \_\_\_\_\_  
 User: \_\_\_\_\_  
 Software: Clarity \_\_\_\_\_  
 Version: \_\_\_\_\_  
 SN: \_\_\_\_\_  
 A/D Board: \_\_\_\_\_  
 Computer: \_\_\_\_\_  
 Operating System: \_\_\_\_\_  
 Validator: \_\_\_\_\_



SST Parameters Table  
 Calculated by Actual Values

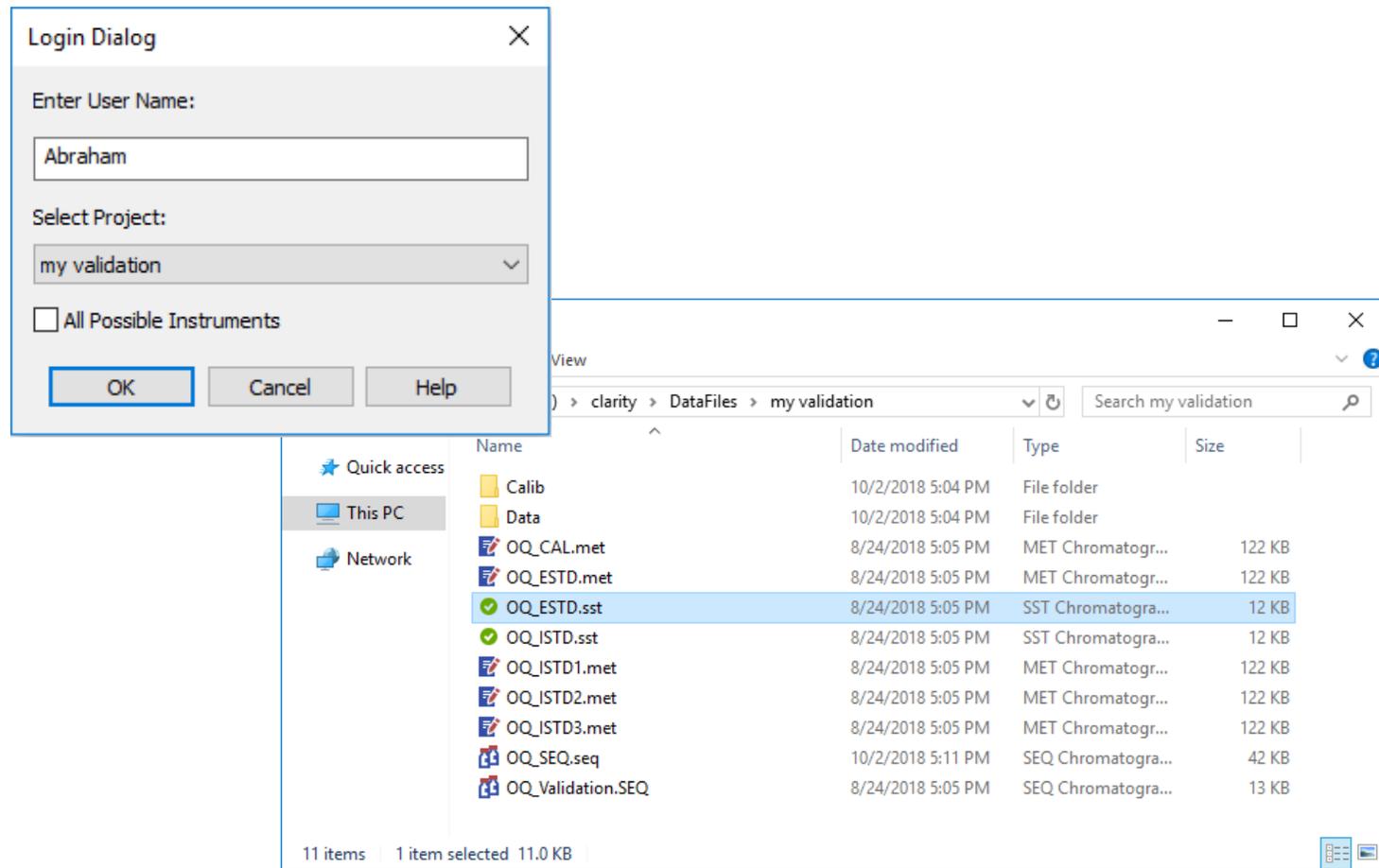
X	SST	Chromatogram	Rate Time
		Lower Limit	1
		Upper Limit	1
		%RSD Limit	1
		Mean	1
		RSD (%)	1
		Parameter Result	1
<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	IQ_Validation_upad2_ESTD_11_22_04_2016_047 - U-PAD2 - 1	1
<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	IQ_Validation_upad2_ESTD_06_22_04_2016_042 - U-PAD2 - 1	1
<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	IQ_Validation_upad2_ESTD_07_22_04_2016_043 - U-PAD2 - 1	1
<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	IQ_Validation_upad2_ESTD_08_22_04_2016_044 - U-PAD2 - 1	1
<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	IQ_Validation_upad2_ESTD_09_22_04_2016_045 - U-PAD2 - 1	1
<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	IQ_Validation_upad2_ESTD_10_22_04_2016_046 - U-PAD2 - 1	1

The chromatography data station specified above was properly installed and its functionality verified by procedure developed by the DataApex, Ltd. This certificate confirms, that the station is functional and performs according to manufacturer's specifications. Part of this certificate are the *Installation Qualification Report* and properly checked and signed records of the *Calibration Linearity Test*, *ESTD Calculation Test* & *ISTD Calculation Test*.  
 The Validation was performed by ...

Validation performed by: \_\_\_\_\_ Signature: \_\_\_\_\_ Date: \_\_\_\_\_  
 User: \_\_\_\_\_ Signature: \_\_\_\_\_ Date: \_\_\_\_\_

➔ After each set of analyses, Clarity generates a multipage OQ report in PDF format ready to be date stamped and signed

➔ These reports are located in the Validation project directory (specified at the start)



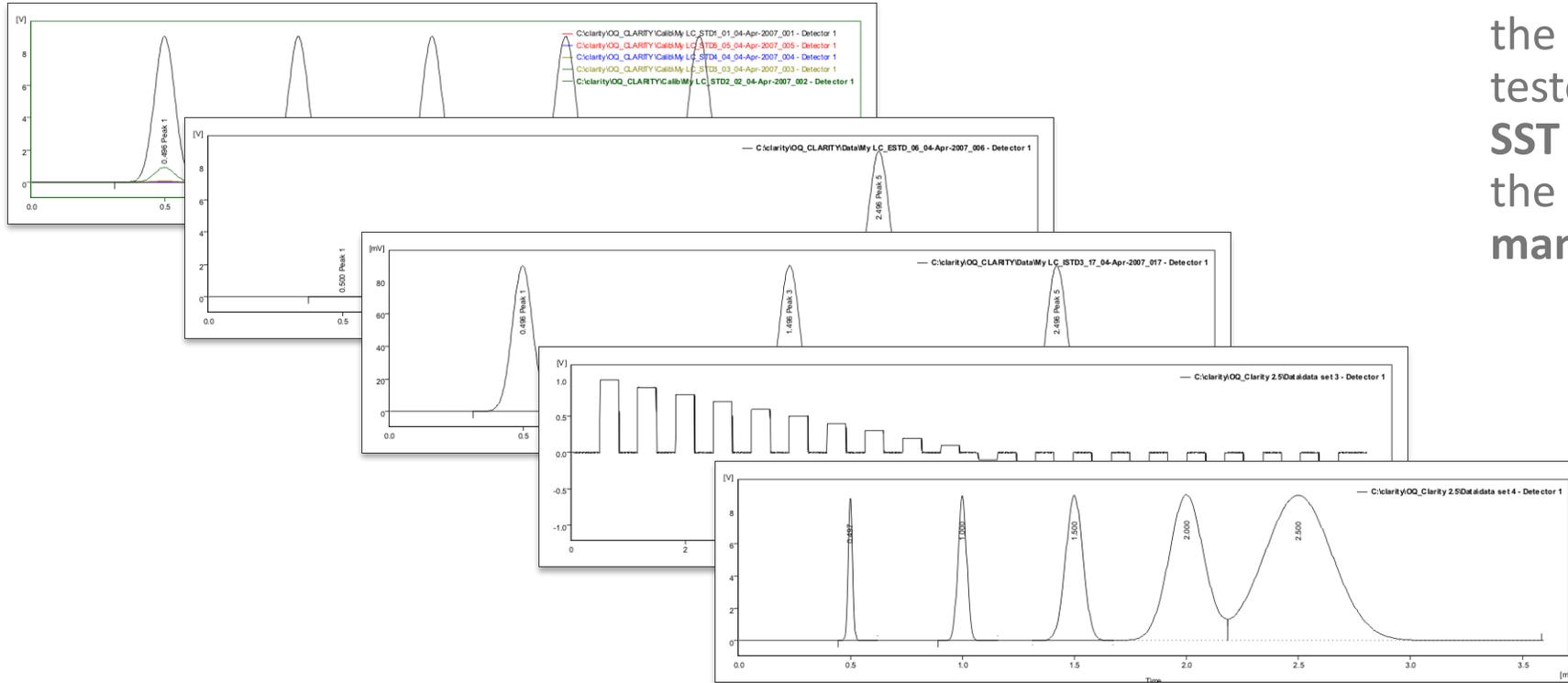
- Your **validation project** is **accessible as any** other Clarity project
- In the project folder you will find chromatograms, methods, calibration files, sequences, reports etc.



- **Service Personnel trained** by DataApex in the use of the Validation Kit will receive a **certificate entitling them to perform validations**



→ More information on the **OQ chromatograms**, tested **parameters** and **SST limits** is located in the **Validation Kit manual**

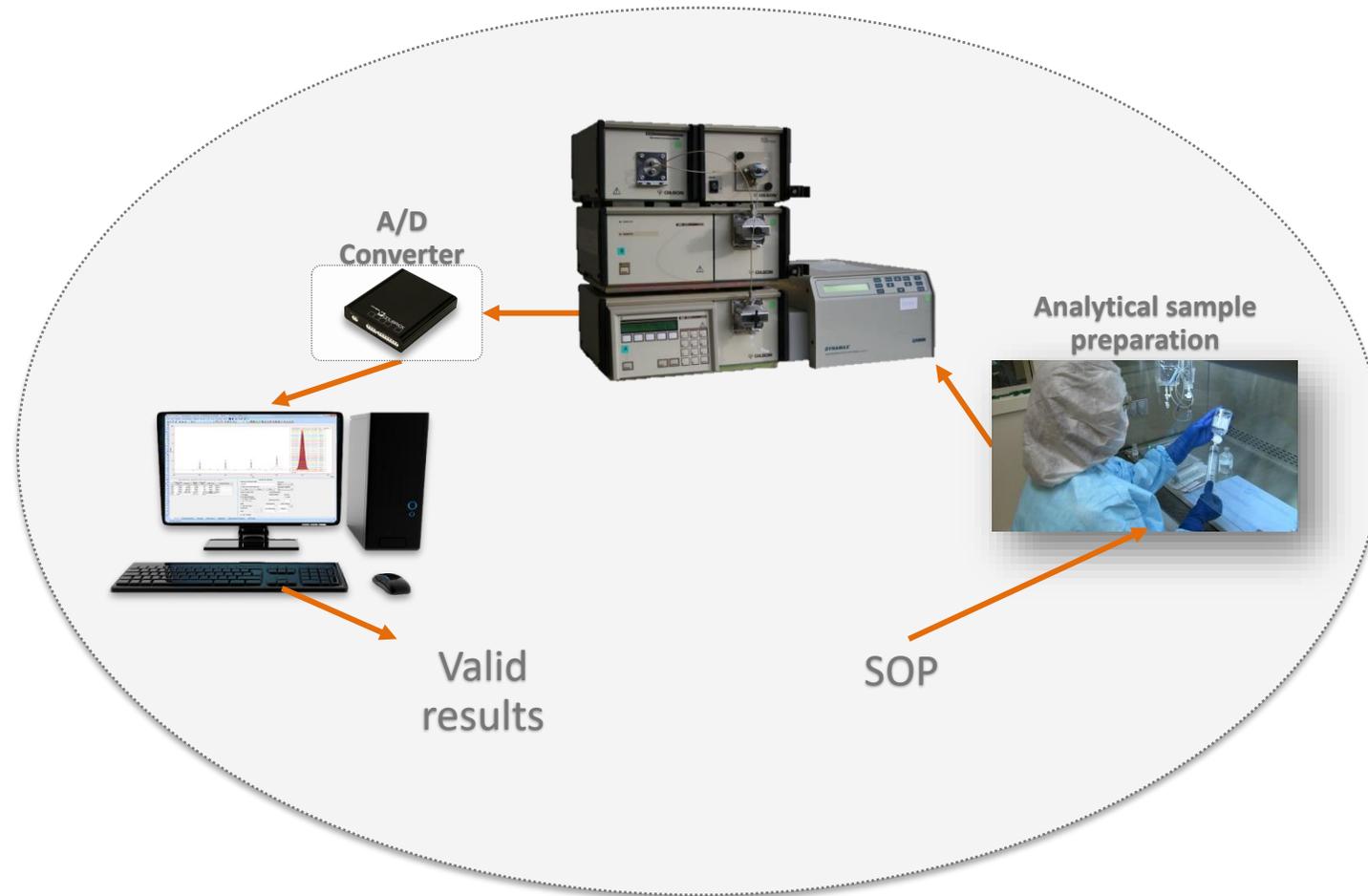


Parameter	Expected value
Calibration curve slope	500 +/- 2
Calibration curve intercept	0,0 +/- 0,5
Correlation factor	min 0,999999

Peak	Limit	RT min	Area mV.s	Height mV	Amount
PEAK 1	Lower	0,49	4,5	0,85	0,009
	Upper	0,51	5,5	0,95	0,011
	RSD%	1,5	2	2	2

# Performance Qualification

- Confirms that the analytical system complies with the standards (SOP) set for a specific type of analysis
- SST can be used for the purposes of PQ but the tests and criteria must be prepared by the laboratory personnel based on the specification of the instrument set by the manufacturer



- PQ is not tested nor any certificate is issued by DataApex
- Before the PQ, the system must pass the IQ and the OQ first



**...THANK YOU FOR YOUR TIME**



[SUPPORT@DATAAPEX.COM](mailto:SUPPORT@DATAAPEX.COM)  
[WWW.DATAAPEX.COM](http://WWW.DATAAPEX.COM)